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Curriculum Vitae

Kim Guldstrand Larsen

Personal Information

Born:	23. December 1957	Nationality:	Danish
Married to:	Merete K. Hansen	Children:	Mia (15), Trine (11)

Addresses

Office:	Department of Math. & Comp. Sc. Aalborg University Fredrik Bajersvej 7 9220 Aalborg, DENMARK	Home:	Skovmarken 4 9510 Arden, DENMARK
Phone:	+45 98 15 42 11 (5017)	Phone:	+45 98 56 22 59
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Academic degrees

Cand.Scient	Aalborg University (Mathematics)	1982
Ph.D	Edinburgh University (Computer Science)	1986
Honorary Doctor (Honoris causa)	Uppsala University	1999

Positions held

Adjunkt–Lektor vikar	Aalborg University	1982.
Research Assistent	Carnegie Mellon University, U.S.A.	1982–83.
Kandidatstipendiat	Aarhus University	1982-85.
Adjunkt	Aalborg University	1985–86.
Lektor	Aalborg University	1986–89.
Visiting Professor	SICS, Sweeden	1990
Seniorstipendiat	Aalborg University	1989–92.
Lektor	Aalborg University	1992–93
Visiting Professor	Uppsala University, Sweeden	1995
Professor	Aalborg University	1993—.
Industrial Professor (part-time)	University of Twente	2000–2002

Admistrative Duties

1987–89	Co–chairman of the Department of Mathematics and Computer Science, Aalborg University.
1991–97	Member of Danish Natural Science Research Council.
1996–	Administrator of the Computer Science and Engineering Ph.D. Programme, Aalborg University
1996–	Chairman for 'Bonusudvalget' (Danish Natural Science Research Council)
1998–	Director of Aalborg section of BRICS
1999–	Member of the Steering Committee for European Educational Forum
1999–	Member of the Steering Committee for the Danish Research Net.
1999–	Member of the Board of Directors for Institute of Computer Science, Aalborg.

Projects

1988–90	TAU–project (with Arne Skou)
.	Supported by the FTU–programme of The Danish Natural Science Research Council.
1988–92	Programming Environments: Theory and Practice (with Bent Bruun Kristensen)
	Supported by a special Basic Research Foundation at Aalborg University.
1989–92	Software Engeneering Programme.
	Supported by the Danish Natural Science Research Council.
1990–93	Member of the DART project.
	Supported by The Danish Natural science Research Council.
1993–95	Scientific Manager for CONCUR2 Esprit BRA project no 7166.
1994–98	Kernel Researcher at the BRICS Centre.
	Sponsered by the Danish National Research Foundation.
1998–03	Director of the Aalborg-section of BRICS
1998–01	VHS (Verification of Hybrid Systems).
	Esprit project 26270 under the Long Term Research Reactive Scheme.

Industrial Projects

1997–99	VVS (Verification and Validation of large Systems).
	with Computer Systems Section DTU, and Baan Visualstate.
	Sponsored by Center for Information Technology.
1996-97	Formal modelling, error-detection and -correction of an IR-link protocol
	for Bang&Olufsen
1997	Design, validation and implementation of a Power-Down Protocol
	for Bang&Olufsen

Organization of Conferences & Workshops.

2nd Nordic Workshop on Program Correctenss, Aalborg University Center, October 3–5, 1990.
3rd workshop on Computer Aided Verification, Aalborg University Center, July, 1991.
6nd Nordic Workshop on Program Correctenss, Aarhus University, October 17–19, 1994.
1st Workshop on Tools and Algoritms for the construction and Analysis of Systems, Aarhus University, 19–20 May, 1995.
BRICS Autumn School on Verification, October 28 — November 1, 1996, Aarhus University.
25'th Internal Colloquium on Automata, Languages and Programming, ICALP98, 1998.
CONCUR 2001, BRICS, Aalborg Universitet, August, 2001.

Invited Lectures

Programming Logics, Marstrand, Sweden, 1987
Systematic Design and Implementation of Communication Protocols, Hansholm, Denmark, 1987.
Acadimia Sinica, Beijing, China, 1989.
Prgoramming Logics, Båstad, Sweden, 1989.
Stepwise Refinement of Parallel Systems, Holland, 1989.
Automatic Methods for Finite State Systems, Grenoble, France, 1989.
Concurrency and Compositionality, San Miniato, Italy, 1990.
Semantique du Parallilisme, Spring–school, La Roche–Posay, France, 1990.
Distributed Computing, Distributed Ph.D.–course in Denmark, 1990.
CONCUR90, Amsterdam, Holland, 1990.
Concurrency and Compositionality, Goslar, Germany, 1991.
Formal Methods Europe, Odense, 1993.
Algorithms in Automata Theory, Dagstuhl, Germany, 1994.
Specification and Refinement of Reactive Systems, Dagstuhl, Germany, 1994.

EXPRESS, Amsterdam, The Netherlands, 1994.
COST 247 Workshop, Berlin, February 9–10, 1995.
Fundamentals of Computation Theory, Dresden, August 22–25, 1995.
DIMACS Workshop on Verification and control of Hybrid Systems, New Brunswick, U.S.A., October 1995.
IEEE Real Time Systems Symposium 95, Pisa, Italy, December 1995.
FTRTFT, Formal Techniques in Real–Time and Fault–Tolerant Systems, Uppsala, Sweden, September 1996.
Annual Symposium of Nederlandse Vereniging voor Theoretische Informatica, Utrecht, The Netherlands, February 1997.
Mathematical Foundations of Programming Systems, MFPS97, Pittsburgh, U.S.A., March 1997.
IFIP WG2.2, meeting in Graz, September 1997.
Real Time Systems Summerschool, Lidingö, Sweden, August 1998.
PROBMIV'99, Probabilistic Methods in Verification, Eindhoven, The Netherlands, August, 1999.
Verification of LARGE Systems, Chalmers Technical University, Sweden, December, 1999.
MOVEP'00, MOdelisation et VErification des Processus paralleles, Nantes, June, 2000.
21st International Conference on Application and Theory of Petri Nets, Aarhus, June, 2000.

Industrial Lectures

NUTEK, Complex Technical Systems Day, Stockholm, Sweden, August 1997.
VisualSTATE Day, Grundfos, March, 1998.
Application of Formal Methods, Aarhus, June 1998.
Dependable Systems, Nordjysk Informatik Forum, September, 1998.
Verification and Validation of Systems, Nato Technical visit, Aalborg, October, 1998.
Verification and Validation of Systems, Bosch Telecom, Pandrup, November, 1998.
Pålidgelige Parallelle Systemer: Verifikations- og Valideringsværktøjer, IDA, København, April, 1999.
Formal Methods: Automatic Validation and Verification Tools, University of Twente and CMG, August, 1999.

Program Committees

HART, International Workshop on Hybrid and Real–Time Systems, 1997.
TACAS, Tools and Algorithms for the Construction and Analysis of Systems, 1995, 1996, 1997, 1998, 1999, 2000
Logic in Computer Science, 1993.
Nordic Workshop on Program Correctness, 1989–1999
CONCUR, 1990, 1991, 1992, 1994, 1997.
Computer Aided Verification, 1991, 1992, 1993, 1994, 1996, 1997, 1998, 2000
Parle, 1992.
IEEE Real Time Systems Symposium, 1998.
Hybrid Systems Workshop, 1998, 1999.
Formal Techniques for Real Time and Fault Tolerant Systems, 1998.
ICALP98, Programme and Organizing Chairman, 1998.
AMAST workshop on Real Time Systems, 1998.
11th Euromicro Conference on Real-Time Systems, 1999.
Logic in Computer Science, 2000.
DSVV, Distributed System Validation and Verification, 2000.
PAPM, 8th International Workshop on Process Algebra and Performance Modelling, 2000.
SPIN'2000, The 7th International SPIN workshop on Model Checking of Software
ADPM'2000 Automation of Mixed ProcessesÆ Hybrid Dynamic Systems

Boards

Member of the editorial and executive board of the Nordic Journal of Computing.
Steering Committee member for CONCUR.
Steering Committee member for TACAS.

Master Thesis Supervision

Jørgen Olesen,Karsten Vestmar: *Specification og Implementation af Fuldautomatisk Verifikationsværktøj baseret på en Operationel Semantik*, 1986.
Michael Hillerstrøm: *Verification af CCS–Processer*, 1987.
Bent Thomsen: *An Extended Bisimulation Induced by a Preorder on Actions*, 1987.
Jens Chr. Godskesen, Anna Ingolfsdottir, Michael Zeeberg: *From Hennessy–Milner Logic to CCS–processes*, 1987.
Hans Hüttel: *Operational and Denotational Properties of a Modal Process Logic*, 1988.
Anders Børjesson, Helle Damborg: *From Process Algebra Towards a Parallel Programming Language*, 1991.
Ole Høeg Jensen, Christian Jeppesen, Jarl Tuxen Lang: *Model Construction for Hybrid Implicit Specifications*, 1993.
Jørgen H. Andersen, Kåre Jelling Kristoffersen, Jesper Niedermann, Jesper Pedcersen: *Specification of Real Time Systems in Timed Modal Logics*, 1994.
Heidi Gregersen, Henrik E. Jensen: *Design of Real Time Probabilistic Logics*, 1995 (co–supervised with Hans Hüttel).
Palle K. Christensen, Thomas M. Sørensen: *Efficient Model Checking of Timed State/Event Systms*, 1997.
Jesper Gravgaard, Per S. Jensen: *Design and Implementation of NewPaal: A Modelling Language for Real-Time Systems*, 1997.
Steffen B. Andersen, Gerd Behrmann, Claus K. Pedersen, Peter S. Vestergaard: *Reusability and Compositionality applied to Verification of Hierarchical Systems*, 1998.
Peter Christoffersen, Jens K. Jensen: *Modelling Environmment for StateText Systems*, 1999.

Ph.D. Supervisions

Arne Skou: *Validation of Concurrent Processes – with Emphasis on Testing*, 1989.
Liu Xinxin: *Specification and Decomposition in Concurrency*, 1992.
Klaus Havelund: *The Fork Calculus*, 1994.
Jens Chr. Godskesen: *Timed Modal Specifications – A Theory for Verification of Real–Time Concurrent Systems*, 1994.
Josva Kleist: *Models for Object Orientation*, in progress.
Jørgen H. Andersen: *Parameterized Process Algebras and Logics*, 1998.
Kaare Kristoffersen: *Compositional Specification and Analysis of Real–Time and Hybrid Systems*, to be defended January 1999..
Henrik Ejersbo Jensen: *Abstraction Based Verification*, in progress (until summer 1998 visiting M.I.T. for two years).
Augusto Burgueno Arjona: *Model-Checking via Testing and Parametric Analysis of Timed Systems*, supervision as BRICS summer Ph.D. Student, 1997
Brahim Mammas: *Parameterized Verification*, supervision as BRICS summer Ph.D. Student, 1998
Gerd Behrmann: *Validation and Verification*, in progress.
M. Oliver Möller: *Validation and Verification*, in progress.

Evaluation Committees

W.O.D. Griffioen, Nijmegen University, Nijmegen, The Netherland (2000)
Martin Weichert, Chalmers Technical University, Göteborg, Sweden (1999)
Brahim Mammas, Universite Pierre & Marie Curie, LIP6, Paris, France (1999)

Augusto Burgueno Arjona, Onera CERT, Tolulouse, France (1998)
Jesper G. Henriksen, Ph.D. Part A, Aarhus University (1997)
Thomas T. Hildebrand, Ph.D. Part A, Aarhus University (1997)
Anders Sandholm, Ph.D. Part A, Aarhus University (1997)
Pim Kars, Ph.D., Tweente University (1997)
Allan Cheng, Ph.D., Aarhus University (1996)
William Ferreira, Doctor of Philosophy, University of Sussex at Brighton (1996)
Anders P. Ravn, Danish Doctor Degree, Danish Technical University (1995)
Martin Weichert, Lic., Chalmers Technical University (1995)
Claus Torp Jensen, Ph.D., Aarhus University (1994)
Janna Elroanta, Lic. & Ph.D., University of Helsinki (1994)
Hakan Erdogmus, Ph.D., Quebec University, Canada (1993)
Steven Klusener, Ph.D., Centrum for Wiskunde et Informatice, Amsterdam (1993)
Jens Nordal, Ph.D., DTU (1993)
Madhavan Mukund, Aarhus University (1992)
Liang Chen, Ph.D., University of Edinburgh (1992)
Anders Gammelgård, Ph.D. Aarhus University (1992)
Hans Hansson, Ph.D., Uppsala University (1991)
Wang Yi, Ph.D., Chalmers Technical University (1991)
Ivan Christoff, Ph.D. Uppsala University (1991)
Uffe Henrik Engberg, Ph.D., Aarhus University (1989)

PUBLICATIONS

Tools and Software

TAV, 1989-1994: A tool for automatic verification of process algebraic specifications. The tool is based on the use of modal transition systems, enabling partial specifications and allowing for step-wise development of system. The tool is based on the principle of local checking algorithms as introduced in the thesis of 1984.

EPSILON, 1993-95: An extension of the TAV systems allowing for quantitative timing behaviour to be modelled, specified and analyzed. This tool is based directly on the theoretical decidable results of Alur, Dill and Cerans from 1990 and 1992.

UPPAAL, 1995-: UPPAAL is a tool suite for validation and verification of real-time system modeled as networks of timed automata extended with data types (integers, arrays etc). The tools in UPPAAL have WYSIWYG interfaces and features including graphical editing, graphical symbolic simulation and symbolic verification of safety and liveness properties. UPPAAL is developed in collaboration with the Design and Analysis of Real-Time Systems group at Uppsala University, Sweden. The collaboration has resulted in more than 20 papers and 10 industrial case-studies. For more information see http://www.docs.uu.se/docs/rtmv/uppaal/.

CBR (visualSTATE) 1997–: CBR is an algorithmic method allowing for analysing properties of very large statemachines; e.g. systems with more than 1.400 statemachine components (and more than 10^{400} states) are dealt with in approximately 12 minutes. The method has been developed within the VVS project (http://www.it.dtu.dk/ jst/vvs/) and is commercially available in the tool visualSTATE (http://www.visualstate.com/). The method is patent pending.

Books

Kim G. Larsen: "Context Dependent Bisimulation between processes", Ph.D. Theses, Edinburgh Universitet, 1986.

Kim G. Larsen, Arne Skou (editors): "Proceedings of Computer Aided Verification", Lecture notes in Computer Science 575, 1992.

J. Stage, K. Nørmark, K.G. Larsen (editors): "Quality Software: Concepts and Tools", 1993.

U.H. Engberg, K.G. Larsen and P.D. Mosses (editors): "Proceedings of the 6th Nordic Workshop on Programming Theory", NS–94–6 in BRICS Notes Series, 1993.

E. Brinksma, W.R. Cleaveland, K.G. Larsen, T. Margari and B. Steffen (editors): "Tools and Algorithms for the Construction and Analysis of Systems", Lecture notes in Computer Science 1019, 1995.

K.G. Larsen, S. Skyum, G. Winskel: "Automata, Languages and Programming. 25'th International Colloquium, ICALP'98", Lecture Notes in Computer Science 1443, 1998.

Journal Contributions

Kim G. Larsen: "A Context Dependent Equivalence between Processes", In Theoretical Computer Science 49 (1987).

Finn V. Jensen, Kim G. Larsen: "Recursively Defined Domains and their induction Principles", In Theoretical Computer Science 54 (1987).

K.G. Larsen and G. Winskel: "Using Information Systems to Solve Recursive Domain Equations", in Information and Computation vol. 91, no. 2, 1991.

Kim G. Larsen: "Proof Systems for Satisfiability in Hennessy–Milner Logic with Recursion", In Theoretical Computer Science 72 (1990).

Kim G. Larsen, Arne Skou: "Bisimulation Through Probabilistic Testing", in Information and Control vol. 94, no. 1, 1991.

G. Boudol, Kim G. Larsen: "Graphical versus Logical Specifications", to appear in Theoretical Computer Science.

Kim G. Larsen, Bent Thomsen: "Partial Specifications and Compositional Verification", In Theoretical Computer Science 88, pp 15–32, 1991.

Kim G. Larsen, Liu Xinxin: "Compositionality Through an Operational Semantics of Contexts", Journal of Logic Computation, vol. 1 no. 6, pp 761–795, 1991.

Kim G. Larsen, Robin Milner: "A Complete Protocol Verification Using Relativeized Bisimulation", Information and Computation, vo. 99, no. 1, July 1992.

Kim G. Larsen: "The Expressive Power of Implicit Specifications", Theoretical Computer Scinece 114, pp 119–147, 1993.

Klaus Havelund, Kim G. Larsen: "The Fork Calculus", In Nordic Journal of Computing 1, pp 346–363, 1994.

Anders Børjesson, Kim G. Larsen, Arne Skou: "Generality in Design and Compositional Verification using TAV", In Journal of Formal Methods of Systems Design, 6, pp 239–258, 1995.

Kim G. Larsen, Wang Yi: "Time Abstracted Bisimulation: Implicit Specifications and Decidability", Accepted for publication in Information and Computation, 1997.

K.G. Larsen, W. P. Pettersson, W. Yi: "UPPAAL in a Nutshell", in International Journal on Software Tools for Technology Transfer, volume 1, 1997.

K.G. Larsen, B. Steffen, C. Weise: "Continuoius modeling of real-time and hybrid systems: from concepts to tools", in International Journal on Software Tools for Technology Transfer, volume 1, 1997.

J. Lind-Nielsen, H.R. Andersen, G. Behrman, H. Hulgaard, K. Kristoffersen and K.G. Larsen: "Verification of Large State/Event Systems using Compositionality and Dependency Analysis (extended version)", Submitted to Formal Methods in System Design.

Kim G. Larsen, Carsten Weise, Wang Yi and Justin Pearson: "Clock Difference Diagrams (extended version)", In Nordic Journal of Computing 6, pp 271–298, 1999.

J. Staunstrup, K.G. Larsen, H.R. Andersen, H. Hulgaard, G. Behrmann, K. Kristoffersen, J. Lind-Nielsen, H. Leerberg, A. Skou, N.B. Theilgaard: "Practical Verification of Embedded Software", To appear in IEEE Computer.

*T. Hune, K.G. Larsen, P. Pettersson***:** "Guided Synthesis of Control Programs Using UPPAAL", Full version invited to Nordic Journal of Computer Science.

Refereed Conference Papers

Kim G. Larsen, Robin Milner: "Verifying a Protocol Using Relativized Bisimulation", ICALP87, Lecture Notes in Computer Science 267, 1987.

Kim G. Larsen: "Proof Systems for Hennessy–Milner Logic with recursion", in Proceedings of 13'th Colloquium on Treees in Algebra and Programming, Lecture Notes in Computer Science 299, 1988.

Kim G. Larsen, Bent Thomsen: "Compositional Proofs by Partial Specification of Processes", In Proceedings of Mathematical Foundations of Computer Science, Lecture Notes in Computer Science 324, 1988.

Kim G. Larsen, Bent Thomsen: "A Modal Process Logic", i Proceedings of Third Annual Symposium on Logic in Computer Science (LICS), Skotland, 1988.

Hans Hüttel, Kim G. Larsen: "The Use of Static Constructs in A Modal Process Logic", Logic at Botik'89, The Seminar on Logical Foundations of Computer Science, U.S.S.R., 1989. Lecture Notes in Computer Science 363.

Kim G. Larsen, Arne Skou: "Bisimulation Through Probabilistic Testing", in Proceeding of Symposium on Principles of Programming Languages (POPL), Austin, Texas, 1989.

Kim G. Larsen: "Modal Specifications", C^3 workshop on *Automatic Methods for Finite State Systems*, Grenoble, France, Juni 1989. Lecture Notes in Computer Science 407.

Kim G. Larsen: "Compositional Theories based on an Operational Semantics of Contexts", REX workshop on Stepwise Refinement of Parallel Systems, Holland, Maj 1989. Lecture Notes in Computer Science 430.

Jens C. Godskesen, Kim G. Larsen, Michael Zeeberg: "TAV — tools for automatic verification — Users Manual", C^3 workshop on *Automatic Methods for Finite State Systems*, Grenoble, France, Juni 1989. Lecture Notes in Computer Science 407.

Kim G. Larsen, Liu Xinxin: "Compositionality Through an Operational Semantics of Contexts", in Proceedings of ICALP90, Lecture Notes in Computer Science 443, 1990.

Gérard Boudol, Kim G. Larsen: "Graphical versus Logical Specifications", in Proceedings of CAAP90, Lecture Notes in Computer Science 431, 1990.

Kim G. Larsen, Liu Xinxin: "Equation Solving Using Modal Transition Systems", In Proceedings of Logic in Computer Science 1990.

Kim G. Larsen: "Ideal Specification Formalism = Expressivity + Compositionality + Decidability + Testability + $\cdots$", Invited Lecture at CONCUR workshop 1990. Lecture Notes in Computer Science 458, 1990.

Bengt Jonsson, Kim G. Larsen: “On the Complexity of Equation Solving in Process Algebra”, Presented at TAPSOFT’91. In Lecture Notes in Computer Science 493.

Kim G. Larsen: “The Expressive Power of Implicit Specifications”, in Proceedings of ICALP91, Lecture Notes in Computer Science, 1991.

Bengt Jonsson, Kim G. Larsen: “Specification and Refinement of Probabilistic Processes”, in Proceedings of LICS’91, 1991.

U. Holmer, K.G. Larsen, W. Yi: “Decidability of Bisimulation Equivalence between Regular Timed Processes”, presented at CAV’91, in Lecture Notes in Computer Science 575, 1991.

Kim G. Larsen, Arne Skou: “Compositional Verification of Probabilistic Processes”. presented at CONCUR’92, in Lecture Notes in Computer Science 630.

Kim G. Larsen: “Efficient Local Correctness Checking”, in Proceedings of CAV’92, Lecture Notes in Computer Science 663, 1992.

Jens Chr. Godskesen, Kim G. Larsen: “Real–Time Calculi and Expansion Theorems — Extended Abstract”, in Proceedings of NAPAW’92, Springer Verlags series on Workshops in Computing.

Anders Børjesson, Kim G. Larsen, Arne Skou: “Generality in Design and Compositional Verification using TAV”, In Proceedings of FORTE’92, IEEE IFIP Transactions on Formal Description Techniques, V, North–Holland, 1992

Jens Chr. Godskesen, Kim G. Larsen: “Real–Time Calculi and Expansion Theorems”, In Proceedings of FST/TCS’92, Lecture Notes in Computer Science 652, 1992.

Wang Yi, Kim G. Larsen: “Testing Probabilistic and Nondeterministic Processes”, in Proceedings of PSTV’92, 1992.

Kim G. Larsen, Wang Yi: “Time Abstracted Bisimulation: Implicit Specifications and Decidability”, in Proceedings of Mathematical Foundations of Programming Systems 1993, Lecture Notes in Computer Science 802, 1993.

Klaus Havelund, Kim G. Larsen: “The Fork Calculus”, in Proceedings of ICALP’93, Lecture Notes in Computer Science 700, 1993.

Karlis Cerans, Jens Chr. Godskesen, Kim G. Larsen: “Timed Modal Specifications — Theory and Tools”, in Proceedings of CAV’93, Lecture Notes in Computer Science 697, 1993.

Ole H. Jensen, Chr. Jeppesen, Jarl Lang, Kim G. Larsen: “Model Construction for Implicit Specifications in Modal Logic”, in Proceedings of CONCUR’93, Lecture Notes in Computer Science 715, 1993.

Jens Chr. Godskesen, Kim G. Larsen, Arne Skou “Automatic Verification of Real–Time Systems Using EPSILON”, in Proceedings of PSTV’94, 1994.

Klaus Havelund, Kim G. Larsen: “A Refinement Logic for the Fork Calculus”, in Proceedings of PSTV’94.

Jens Chr. Godskesen, K.G. Larsen: “Syntehsizing Distinguishing Formulae for Real Time Systems”, In Proceedings of Mathematical Foundations of Computer Science 1995, Lecture Notes in Computer Science 969, 1995.

J.H. Andersen, K.J. Kristoffersen, K.G. Larsen, J. Niedermann: "Automatic Synthesis of Real Time Systems", In Proceedings of 22nd International Colloquium on Automata, Languages and Programming ICALP'95. Lecture Notes in Computer Science 944, 1995.

K.G.Larsen, B.Steffen, C. Weise: "A Constraint Oriented Proof Methodology Based on Modal Transition Systems", Tools and Algorithms for the Construction and Analysis of Systems. Lecture Notes in Computer Science 1019, 1995.

K.G. Larsen, F. Laroussinie, C.Weise: "From Timed Automata to Logic — and Back", In Proceedings of Mathematical Foundations of Computer Science 1995. Lecture Notes in Computer Science 969. 1995.

K.G. Larsen, F. Laroussinie: "Compositional Model Checking of Real Time Systems" In Proceedings of CONCUR'95. Lecture Notes in Computer Science 962, 1995.

K.G. Larsen, P. Pettersson and W. Yi "Model–Checking for Real–Time Systems", Invited paper to Fundamentals of Computation Theory, Dresden. Lecture Notes in Computer Science 965. 1995.

J. Bengtsson, K.G. Larsen, F. Larsson, P. Pettersson, W. Yi "UPPAAL: a tool suite for automatic verification of real–time systems", Hybrid Systems III, Verification and Control, DIMACS. Lecture Notes in Computer Science 1066, 1996.

K.G. Larsen, P. Pettersson, W. Yi "Diagnostic Model–Checking for Real–Time Systems", Hybrid Systems III, Verification and Control, DIMACS. Lecture Notes in Computer Science 1066, 1996.

K.G. Larsen, B. Steffen, C. Weise "Fischer's Protocol Revisited: a simple proof using modal constraints", Hybrid Systems III, Verification and Control, DIMACS. Lecture Notes in Computer Science 1066, 1996.

J. Bengtsson, W.O.D. Griffioen, K.J.Kristoffersen, K.G. Larsen, F. Larsson, P. Pettersson, W. Yi "Verification of an Audio Protocol with Bus Collision using UPPAAL", Computer Aided Verification. Lecture Notes in Computer Science 1102, 1996.

J. Bengtsson, K.G. Larsen, P.Pettersson, W. Yi "UPPAAL in 1995", Tools and Algorithms for the Construction and Analysis of Systems. Lecture Notes in Computer Science 1055, 1996.

H.E. Jensen, K.G. Larsen, A. Skou "Modelling and Analysis of a Collision Avoidance Protocol using SPIN and UPPAAL", In Proceedings of 2nd International Workshop on the SPIN Verification System, 1996.

Kim G. Larsen, Bernhard Steffen and Carsten Weise "The Methodology of Modal Constraints", In "Formal Systems Specification: the RPC–memory specification case study", Editors: M. Broy, S. Merz, K. Speis. Lecture Notes in Computer Science 1169, 1996.

K.J. Kristoffersen and F. Laroussinie and K.G. Larsen and P. Pettersson and W. Yi "A Compositional Proof of a Real-Time Mutual Exclusion Protocol", Accepted for publication for TAPSOFT 1997. To be published in Lecture Notes in Computer Science.

Kim G. Larsen, Paul Pettersson and Wang Yi: "UPPAAL: Status and Developments", In Proceedings of the 9th International Conference on Computer-Aided Verification. Haifa, Israel, 22-25 June 1997.

G. Behrmann, K.J. Kristoffersen, K.G. Larsen: "Context Dependent Minimization of State/Event Systems", in Proceedings of Nordic Workshop of Programming Correctness, 1997.

J.H. Andersen, K.G. Larsen : "Compositional Safety Logics", BRICS report RS-97-13.

Kim G. Larsen, Fredrik Larsson, Paul Pettersson and Wang Yi: "Efficient Verification of Real-Time Systems: Compact Data Structure and State-Space Reduction", In Proceedings of the 18th IEEE Real-Time Systems Symposium, pages 14-24. San Francisco, California, USA, 3-5 December 1997.

Klaus Havelund, Arne Skou, Kim G. Larsen and Kristian Lund: "Formal Modelling and Analysis of an Audio/Video Protocol: An Industrial Case Study Using UPPAAL.", In Proceedings of the 18th IEEE Real-Time Systems Symposium, pages 2-13. San Francisco, California, USA, 3-5 December 1997.

Luca Aceto, Augusto Bergueno and Kim G. Larsen: "Model Checking via Reachability Testing for Timed Automata. ", In Proceedings of the 4th International Workshop on Tools and Algorithms for the Construction and Analysis of Systems. Gulbenkian Foundation, Lisbon , Portugal, 31 March - 2 April, 1998. LNCS 1384.

J. Lind-Nielsen, H.R. Andersen, G. Behrman, H. Hulgaard, K. Kristoffersen and K.G. Larsen: "Verification of Large State/Event Systems using Compositionality and Dependency Analysis", In Proceedings of the 4th International Workshop on Tools and Algorithms for the Construction and Analysis of Systems. Gulbenkian Foundation, Lisbon , Portugal, 31 March - 2 April, 1998. LNCS 1384.

Johan Bengtsson, Kim G. Larsen, Fredrik Larsson, Paul Pettersson, Wang Yi and Carsten Weise.: "New Generation of UPPAAL." In proceedings of the International Workshop on Software Tools for Technology Transfer. Aalborg, Denmark, 12 - 13 July, 1998.

Kim G. Larsen, Carsten Weise, Wang Yi and Justin Pearson: "Clock Difference Diagrams.", In Proceedings of Nordic Workshop on Programming Theory, Turku, 1998.

F. Laroussinie, K.G. Larsen: "CMC: A Tool for Compositional Model-Checking of Real-Time Systems.", In Proceedings of FORTE/PSTV Conference, Paris, 1998.

L. Aceto, P. Bouyer, A. Burgueno, K.G. Larsen: "The Limit of Testing for Timed Automata", In Proceedings of Foundations of Software Technology and Theoretical Computer Science, India, 1998. Lecture Notes in Computer Science 1530.

G. Behrman, K.G. Larsen, H.R. Andersen, H. Hulgaard and J. Lind-Nielsen: "Verification of Hierarchical State/Event Systems using Reusability and Compositionality", In Proceedings of TACAS'99. Lecture Notes in Computer Science 1579.

K. Havelund, K.G. Larsen, A. Skou: "Formal Verification of a Power Controller Using the Real-Time Model Checker UPPAAL", In Proceedings of 5th International AMAST Workshop, ARTS'99. May 1999. Lecture Notes in Computer Science 1601.

G. Behrmann, K.G. Larsen, J. Pearson, C. Weise, W. Yi: "Efficient Timed Reachability Analysis Using Clock Difference Diagrams", In Proceedings of Computer Aided Verification 1999. Lecture Notes in Computer Science 1633.

T. Hune, K.G. Larsen, P. Pettersson: "Guided Synthesis of Control Programs Using UPPAAL", Accepted for presentation at the International Workshop on Distributed Systems Verification and Validation, 2000.

T. Iversen, K. Kristoffersen, K.G. Larsen, M. Laursen, R.G. Madsen, S.K. Mortensen, P. Pettersson, C.B. Thomasen: "Model-Checking Real-Time Control Programs", Submitted, 1999.

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