Semantics and Verification 2006

Lecture 15

- round-up of the course
- information about the exam
- selection of star exercises

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Calculus of Communicating Systems

CCS

Process algebra called "Calculus of Communicating Systems".

Insight of Robin Milner (1989)

Concurrent (parallel) processes have an algebraic structure.

$$P_1$$
 op P_2 \Rightarrow P_1 op P_2

Reactive systems

Characterization of a Reactive System

Reactive System is a system that computes by reacting to stimuli from its environment.

Key Issues:

- parallelism
- communication and interaction

Nontermination is good!

The result (if any) does not have to be unique.

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Reactive/Parallel

often desirable

LTS

Syntax of CCS

Process expressions:

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Classical vs. Reactive Computing

interaction

nontermination

unique result

semantics

$$P := \begin{array}{c|c} K & \text{process constants } (K \in \mathcal{K}) \\ \alpha.P & \text{prefixing } (\alpha \in Act) \\ \sum_{i \in I} P_i & \text{summation } (I \text{ is an arbitrary index set}) \\ P_1 | P_2 & \text{parallel composition} \\ P \smallsetminus L & \text{restriction } (L \subseteq \mathcal{A}) \\ P[f] & \text{relabelling } (f : Act \to Act) \text{ such that} \\ \bullet & f(\tau) = \tau \\ \bullet & f(\overline{a}) = \overline{f(a)} \\ P_1 + P_2 = \sum_{i \in \{1,2\}} P_i & \text{NiI} = 0 = \sum_{i \in \emptyset} P_i \end{array}$$

Classical

no

undesirable

yes

 $states \hookrightarrow states$

CCS program

A collection of **defining equations** of the form $K \stackrel{\mathrm{def}}{=} P$ where $K \in \mathcal{K}$ is a process constant and P is a process expression.

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Process Algebras

Basic Principle

- ① Define a few atomic processes (modelling the simplest process behaviour).
- ② Define compositionally new operations (building more complex process behaviour from simple ones).

Example

- ① atomic instruction: assignment (e.g. x:=2 and x:=x+2)
- 2 new operators: sequential composition $(P_1; P_2)$ parallel composition $(P_1 | P_2)$

Usually given by abstract syntax:

$$P, P_1, P_2 ::= x := e \mid P_1; P_2 \mid P_1 \mid P_2$$

where x ranges over variables and e over arithmetical expressions.

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Semantics of CCS — SOS rules ($\alpha \in Act$, $a \in \mathcal{L}$)

$$\mathsf{ACT} \ \, \frac{}{\alpha.P \overset{\alpha}{\longrightarrow} P} \qquad \qquad \mathsf{SUM}_j \ \, \frac{P_j \overset{\alpha}{\longrightarrow} P_j'}{\sum_{i \in I} P_i \overset{\alpha}{\longrightarrow} P_j'} \ \, j \in I$$

$$\mathsf{COM1} \ \ \frac{P \overset{\alpha}{\longrightarrow} P'}{P|Q \overset{\alpha}{\longrightarrow} P'|Q} \qquad \qquad \mathsf{COM2} \ \ \frac{Q \overset{\alpha}{\longrightarrow} Q'}{P|Q \overset{\alpha}{\longrightarrow} P|Q'}$$

COM3
$$\xrightarrow{P \xrightarrow{\overline{a}} P'} Q \xrightarrow{\overline{a}} Q'$$

 $P|Q \xrightarrow{\tau} P'|Q'$

$$\mathsf{RES} \ \, \frac{P \overset{\alpha}{\longrightarrow} P'}{P \smallsetminus L \overset{\alpha}{\longrightarrow} P' \smallsetminus L} \ \, \alpha, \overline{\alpha} \not\in L \qquad \quad \mathsf{REL} \ \, \frac{P \overset{\alpha}{\longrightarrow} P'}{P[f] \overset{f(\alpha)}{\longrightarrow} P'[f]}$$

$$CON \xrightarrow{P \xrightarrow{\alpha} P'} K \stackrel{\text{def}}{=} P$$

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Introducing Time Features

In many applications, we would like to explicitly model real-time in our models.

Timed (labelled) transition system

Timed LTS is an ordinary LTS where actions are of the form $Act = L \cup \mathbb{R}^{\geq 0}$.

- $s \xrightarrow{a} s'$ for $a \in L$ are discrete transitions
- $s \xrightarrow{d} s'$ for $d \in \mathbb{R}^{\geq 0}$ are time-elapsing (delay) transitions

Verification Approaches

Let Impl be an implementation of a system (e.g. in CCS syntax).

Equivalence Checking Approach

$$Impl \equiv Spec$$

- Spec is a full specification of the intended behaviour
- Example: $s \sim t$ or $s \approx t$

Model Checking Approach

$$Impl \models Propertv$$

- Property is a partial specification of the intended behaviour
- Example: $s \models \langle a \rangle ([b]ff \land \langle a \rangle tt)$

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approaches.

Theorem (Hennessy-Milner)

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Timed and Untimed Bisimilarity

Let s and t be two states in timed LTS.

Timed Bisimilarity (= Strong Bisimilarity)

We say that s and t are **timed bisimilar** iff $s \sim t$.

Remark: all transitions are considered as visible transitions.

Untimed Bisimilarity

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We say that s and t are **untimed bisimilar** iff $s \sim t$ in a modified transition system where every transition of the form $\stackrel{d}{\longrightarrow}$ for $d \in \mathbb{R}^{\geq 0}$ is replaced by a transition $\stackrel{\epsilon}{\longrightarrow}$ for a new (single) action ϵ .

Remark:

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- $\stackrel{a}{\longrightarrow}$ for $a \in L$ are treated as visible transitions, while
- $\bullet \xrightarrow{d}$ for $d \in \mathbb{R}^{\geq 0}$ all look the same (action ϵ).

 $p \sim a$

if and only if

for every HM formula F (even with recursion): $(p \models F \iff q \models F)$.

Relationship between Equivalence and Model Checking

Equivalence checking and model checking are complementary

Timed CCS — a Way to Define Timed LTS

They are strongly connected, however.

Let us consider an image-finite LTS. Then

Syntax of CCS with Time Delays All CCS operators +

if P is a process then $\epsilon(d).P$ is also a process for any nonnegative real number d

Semantics of CCS with Time Delays

By means of SOS rules

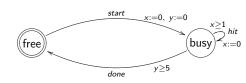
- standard CCS rules
- SOS rules for time delays (maximal progress assumption)

we describe for a given TCCS expression what is the corresponding timed transition system.

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Timed Automata — a Way to Define Timed LTS

- Nondeterministic finite-state automata with additional time features (clocks).
- Clocks can be tested against constants or compared to each other (pairwise).
- Executing a transition can reset selected clocks.



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The End

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The course is over now!

Region Graph — a Verification Technique for TA

We introduce an equivalence on clock valuations ($v \equiv v'$) with **finitely** many equivalence classes.

state
$$(\ell, v) \longrightarrow \text{symbolic state } (\ell, [v])$$

Region Graph Preserves Untimed Bisimilarity

For every location ℓ and any two valuations v and v' from the same symbolic state ($v \equiv v'$) it holds that (ℓ, v) and (ℓ, v') are untimed bisimilar.

Reduction of Timed Automata Reachability to Region Graphs $(\ell_0, \nu_0) \longrightarrow^* (\ell, \nu)$ in a timed automaton if and only if $(\ell_0, [\nu_0]) \Longrightarrow^* (\ell, [\nu])$ in its (finite) region graph.

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Thanks

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Thank you for proof-reading the book!

Compact Representation of State-Spaces in the Memory

Boolean Functions (where $\mathbb{B} = \{0, 1\}$)

$$f:\mathbb{B}^n\to\mathbb{B}$$

Boolean Expressions

$$t, t_1, t_2 ::= 0 \mid 1 \mid x \mid \neg t \mid t_1 \land t_2 \mid t_1 \lor t_2 \mid t_1 \Rightarrow t_2 \mid t_1 \Leftrightarrow t_2$$

Boolean expression:

 $\neg x_1 \land (x_2 \Rightarrow (x_1 \lor x_3))$



Reduced and Ordered Binary Decision Diagram (ROBDD)

Logical operations on ROBDDs can be done efficiently!

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Information about the Exam

- Oral exam with preparation time, passed/failed.
- Preparation time (20 minutes) for solving a randomly selected star exercise.
- Examination time (20 minutes):
 - presentation of the star exercise (necessary condition for passing)
 - presentation of your randomly selected exam question
 - ► answering questions
 - ▶ evaluation
- 9 exam questions (with possible pensum dispensation).
- For a detailed summary of the reading material check the lectures plan.

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Exam Questions

- Transition systems and CCS.
- 2 Strong and weak bisimilarity, bisimulation games.
- 3 Hennessy-Milner logic and bisimulation.
- Tarski's fixed-point theorem and Hennessy-Milner logic with one recursive formulae.
- ⑤ Alternating bit protocol and its modelling and verification using CWB. (Possible pensum dispensation.)
- Timed CCS and bisimilarity.
- Timed automata.
- Sossiping girls problem and its modelling and verification using UPPAAL. (Possible pensum dispensation.)
- 9 Binary decision diagrams and their applications.

Further details are on the web-page. Check whether you are on the list of students with pensum dispensation!

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Examples of Star Exercises — CCS

• By using SOS rules for CCS prove the existence of the following transition (assume that $A \stackrel{\text{def}}{=} a.A$):

$$((A \mid \overline{a}.Nil) + A) \setminus \{a\} \xrightarrow{\tau} (A \mid Nil) \setminus \{a\}$$

• Draw the LTS generated by the following CCS expression:

$$(\overline{a}.Nil \mid a.Nil) + b.Nil$$

How to Prepare for the Exam?

- Read the recommended material.
- Try to understand all topics equally well (remember you pick up two random topics out of 7).
- Go through all tutorial exercises and try to solve them. (Make sure that you can solve all star exercises fast!)
- Go through the slides to see whether you didn't miss anything.
- Make a summary for each question on a few A4 papers (you can take them at exam).
- Prepare a strategy how to present each question.

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Examples of Star Exercises — Bisimilarity

Determine whether the following two CCS expressions

$$a.(b.Nil + c.Nil)$$
 and $a.(b.Nil + \tau.c.Nil)$

are:

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- strongly bisimilar?
- weakly bisimilar?

Further Tips

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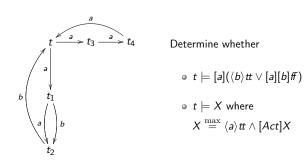
- It does not matter if you make a small error in a star exercise (as long as you understand what you are doing).
- Present a solution to the star exercise quickly (max 5 minutes).
- Start your presentation by writing a road-map (max 4 items).
- Plan your presentation to take about 10 minutes:
 - ► give a good overview
 - ▶ do not start with technical details
 - ▶ use the blackboard

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- ► use examples (be creative)
- ► say only things that you know are correct
- ▶ be ready to answer supplementary questions
- ► tell a story (covering a sufficient part of the exam question)

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Examples of Star Exercises — HML



Find a distinguishing formulae for the CCS expressions:

$$a.a.Nil + a.b.Nil$$
 and $a.(a.Nil + b.Nil)$.

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Examples of Star Exercises — TCCS

Examples of Star Exercises — TA

Examples of Star Exercises — ROBDD

Using the SOS rules for TCCS prove that

$$\epsilon(5).(\epsilon(3).\textit{Nil} + b.\textit{Nil}) \stackrel{7}{\longrightarrow} \epsilon(1).\textit{Nil} + b.\textit{Nil}$$

Draw a region graph of the following timed automaton:



Construct ROBDD for the following boolean expression:

$$x_1 \wedge (\neg x_2 \vee x_1 \vee x_2) \wedge x_3$$

such that $x_1 < x_2 < x_3$.

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